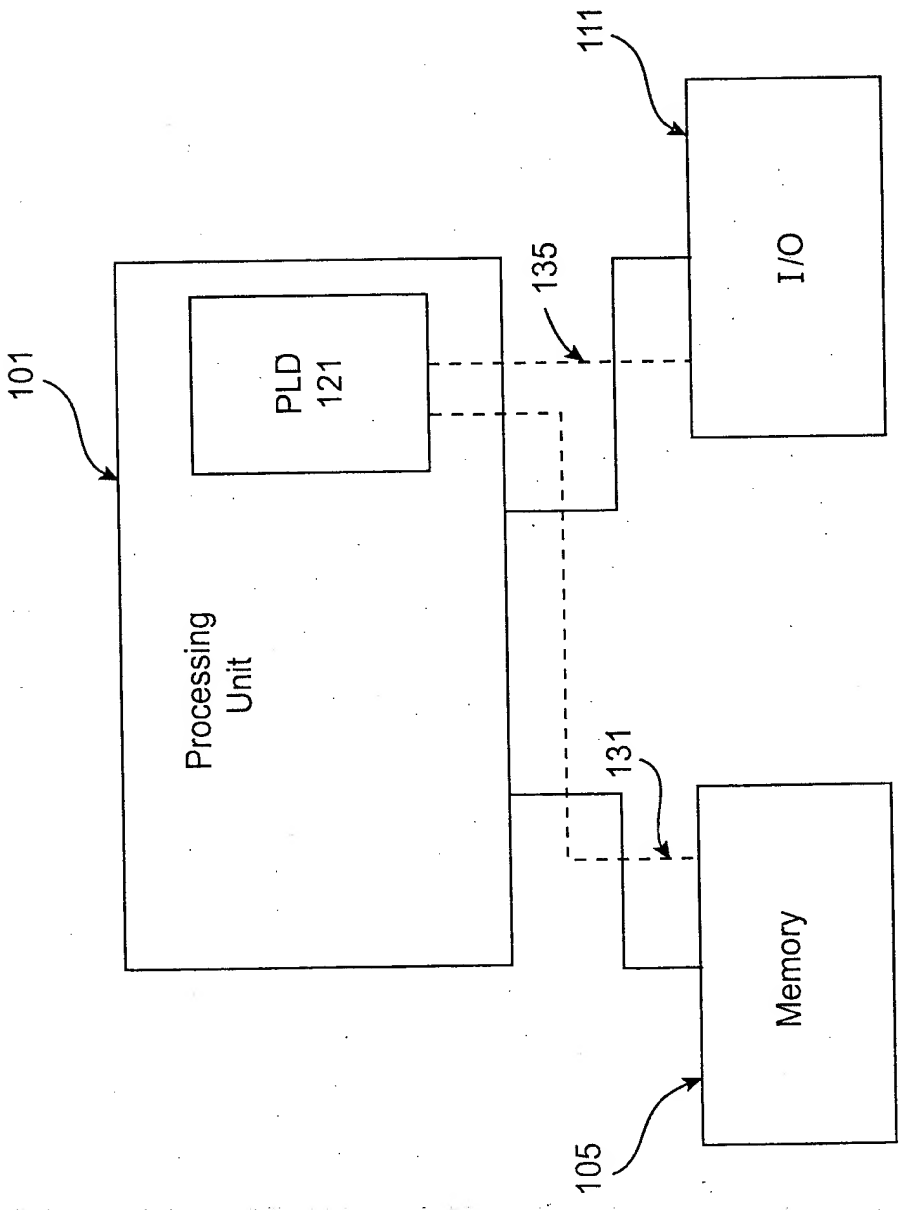


FIG. 1

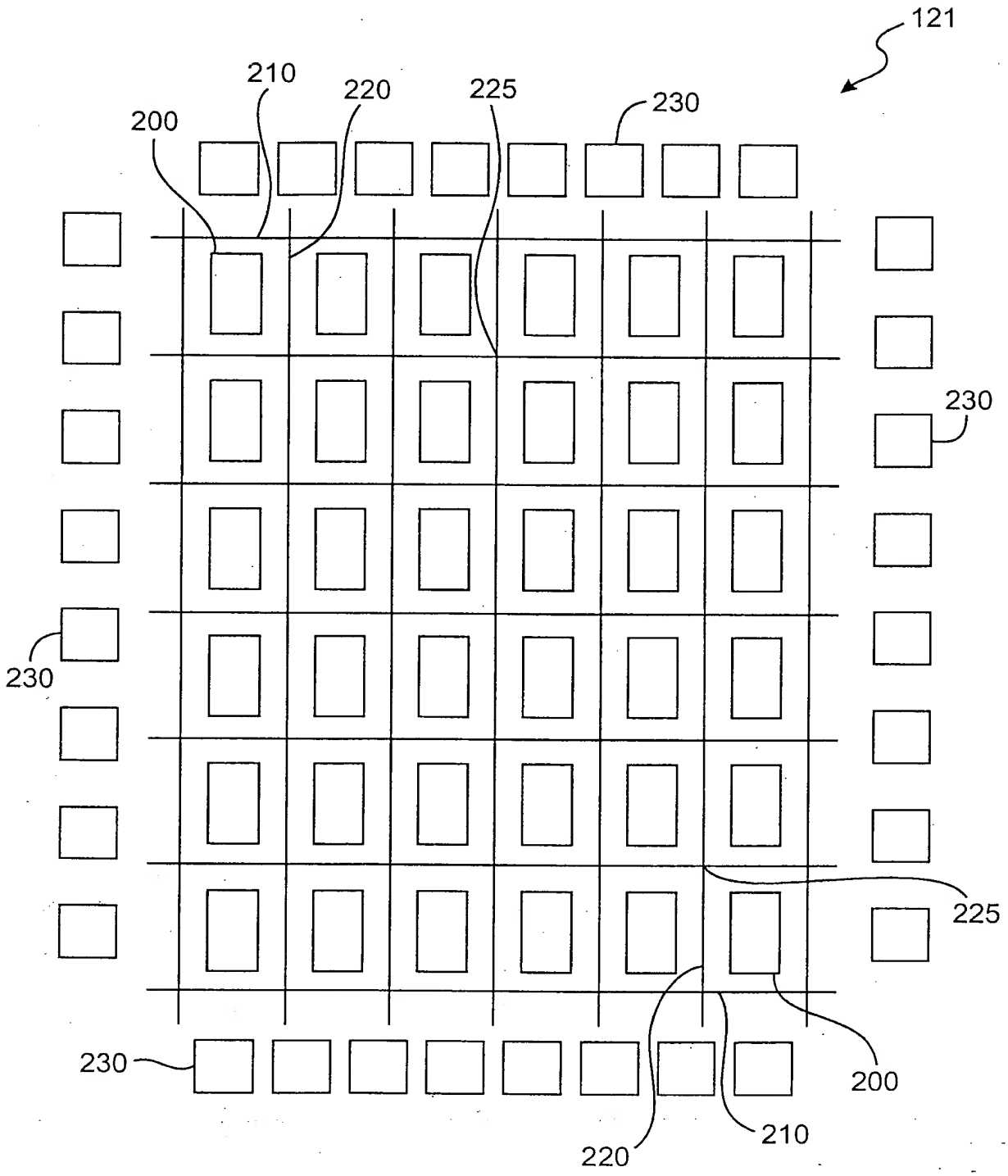


FIG. 2

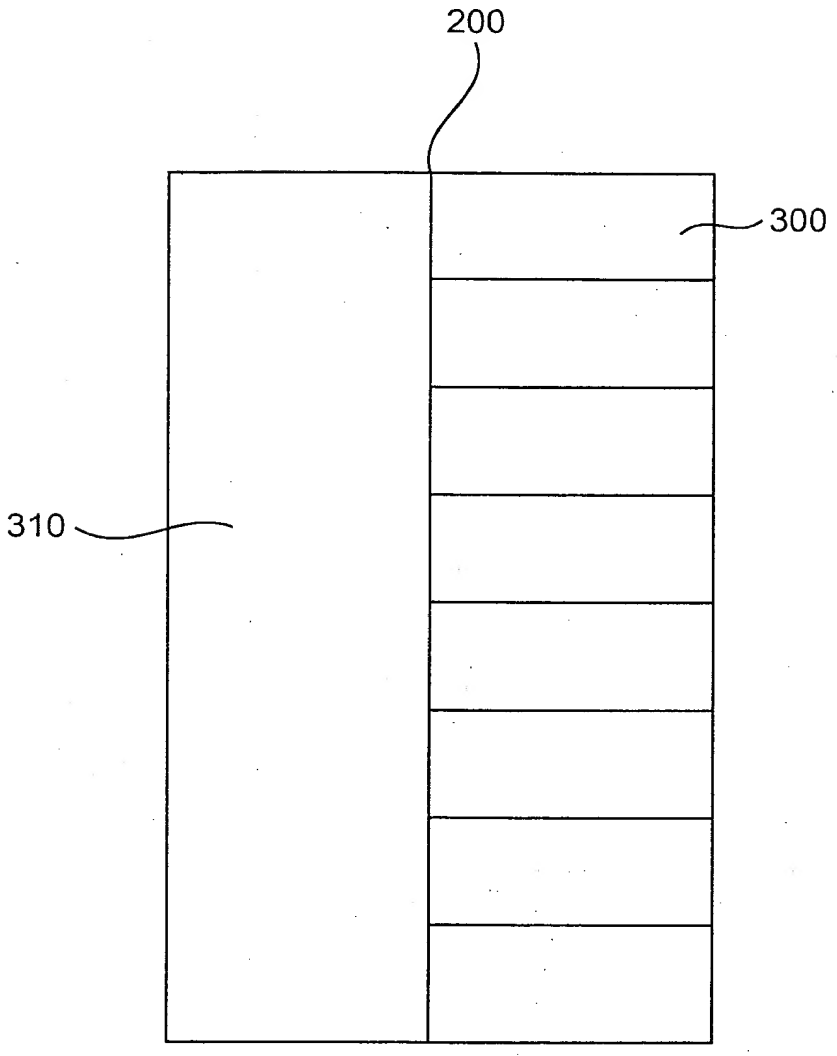


FIG. 3

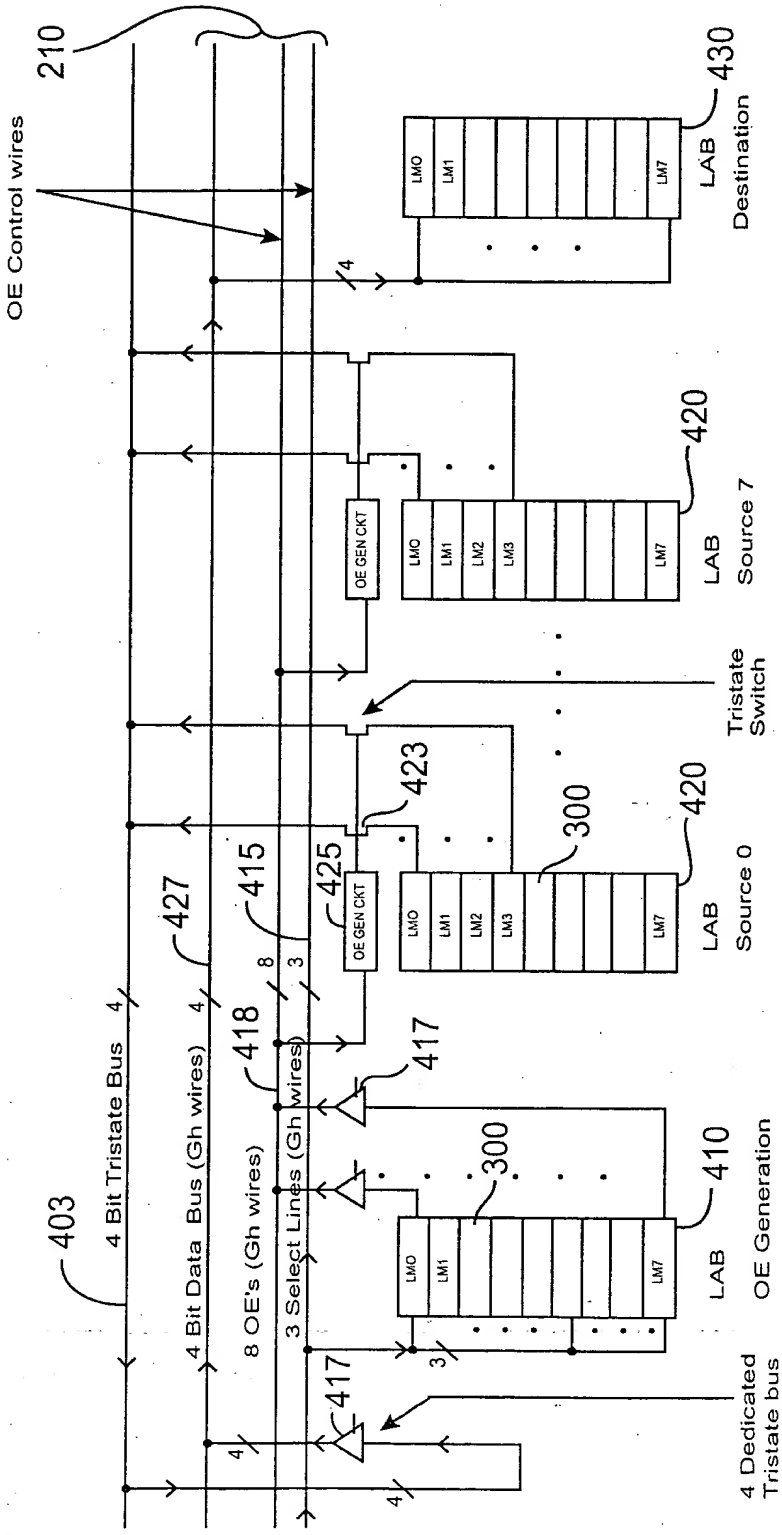

210
OE Control wires
430
LAB
Destination
LM0
LM1
LM7
420
LAB
Source 7
OE GEN CKT
LM2
LM3
Tristate
Switch
423
300
420
LAB
Source 0
425
415
427
418
417
403
4 Bit Tristate Bus
4 Bit Data Bus (Gh wires)
8 OE's (Gh wires)
3 Select Lines (Gh wires)
410
LAB
OE Generation
4 Dedicated
Tristate bus
to Gh bus
drivers

FIG. 4

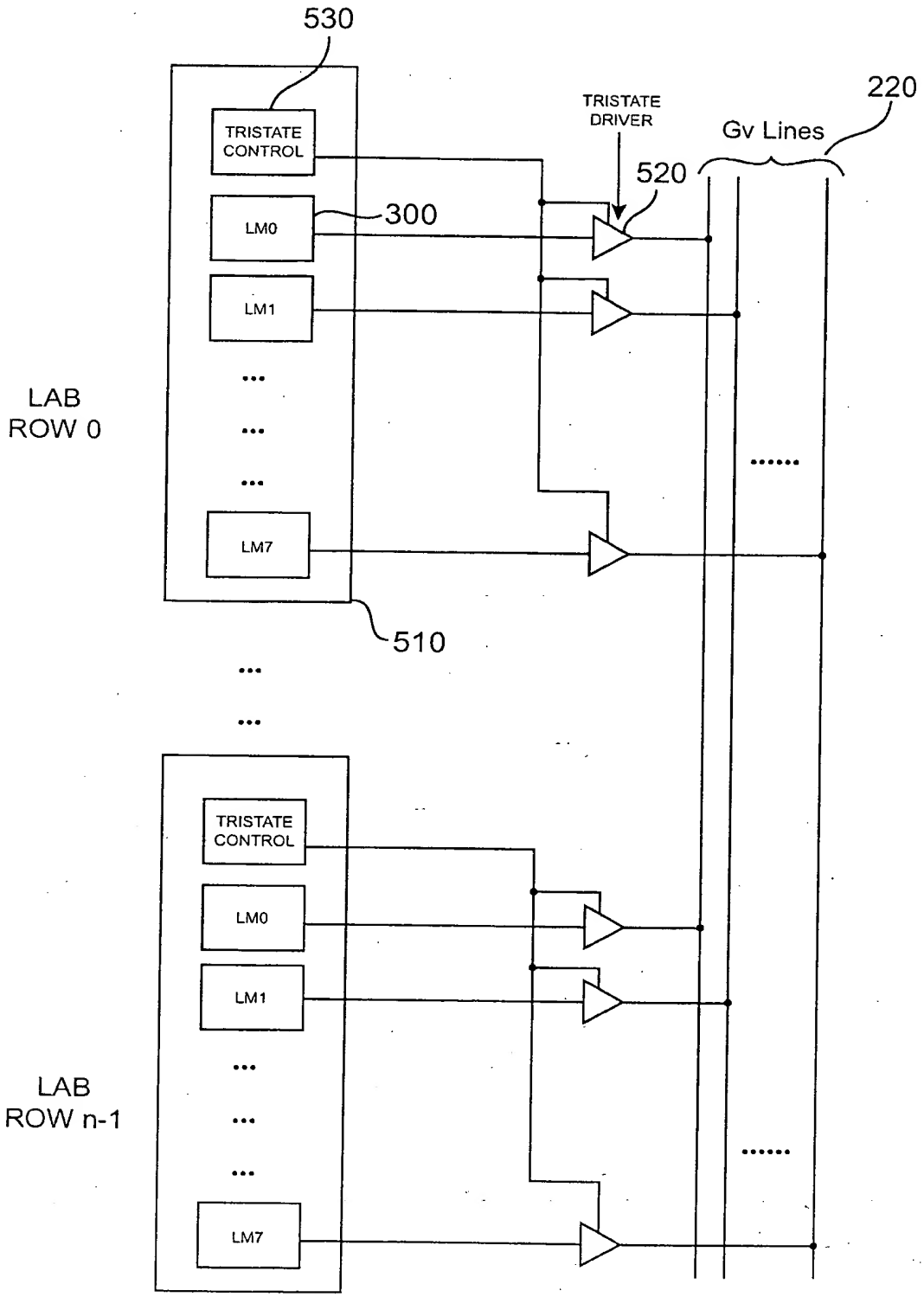

530
TRISTATE CONTROL
LM0
LM1
LM7
300
TRISTATE DRIVER
520
Gv Lines
220
510
LAB ROW 0
LAB ROW n-1

FIG. 5A

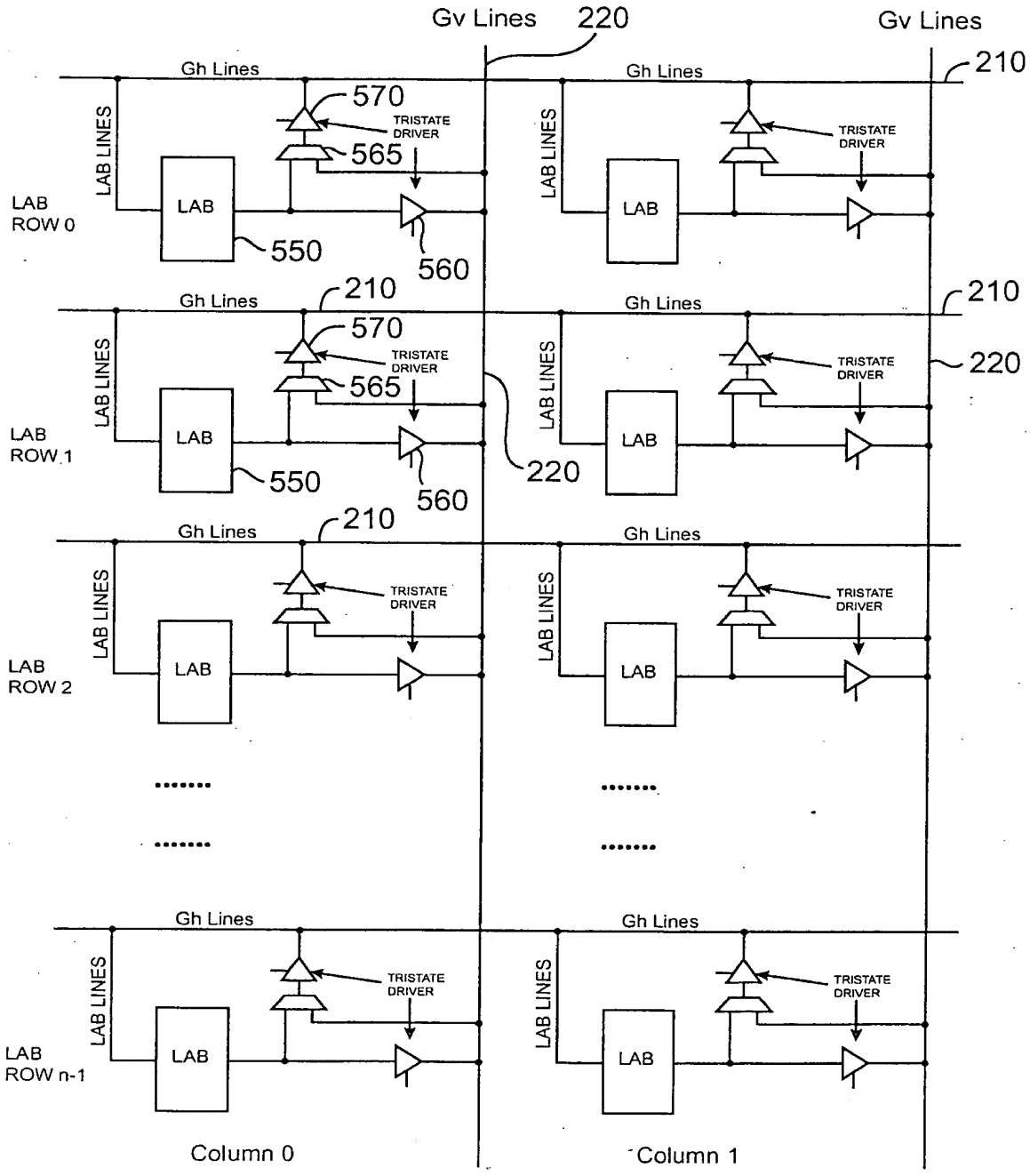
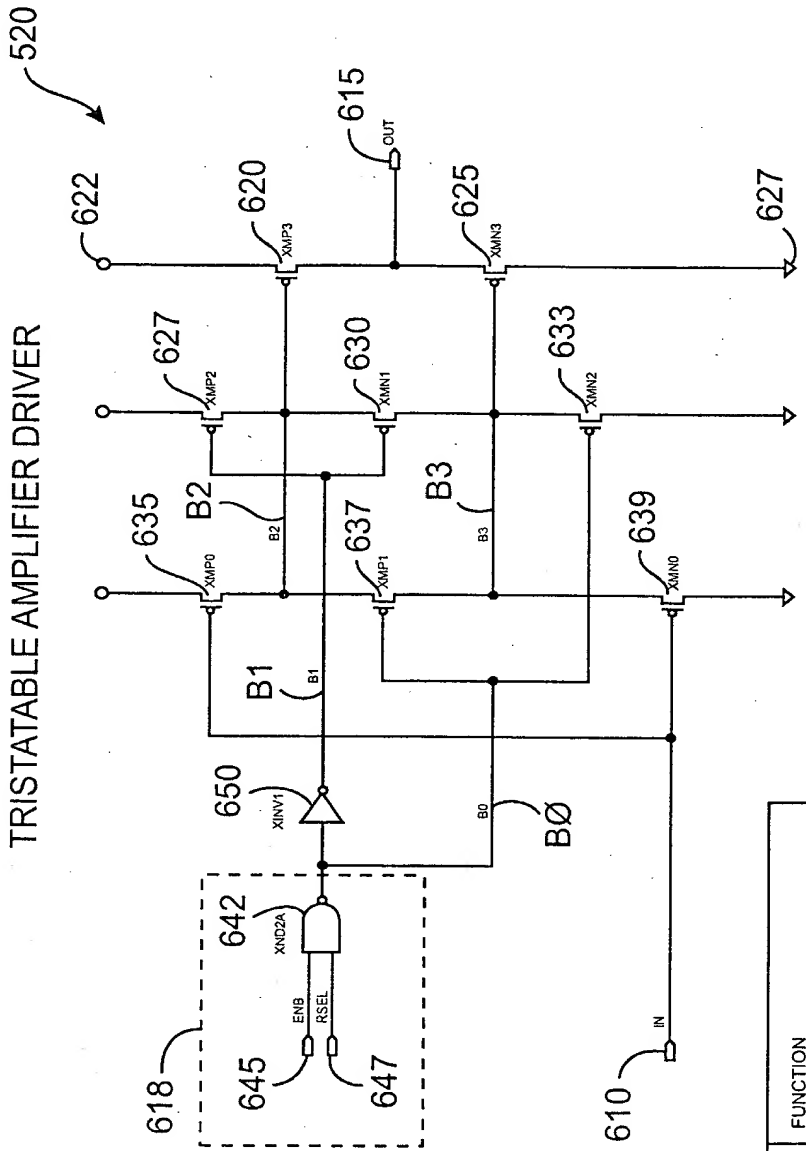

Gv Lines
220
Gv Lines
Gh Lines
210
LAB LINES
570
TRISTATE DRIVER
565
LAB
LAB ROW 0
550
560
LAB ROW 1
LAB ROW 2
LAB ROW n-1
220
Column 0
Column 1

FIG. 5B



RSEL	ENB	OUT	FUNCTION
0	0	TRISTATED	DRIVER DISABLED AND TRISTATED
0	1	TRISTATED	DRIVER DISABLED AND TRISTATED
1	0	TRISTATED	DRIVER ENABLED AND TRISTATED
1	1	IN	DRIVER ENABLED AND DRIVING

FIG. 6